



IFW

Docket No.: 1293.1206

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

Jae-seong SHIM et al.

Serial No. 09/872,155

Group Art Unit: 2611

Confirmation No. 8787

Filed: June 4, 2001

Examiner: Anna Y. Ziskind

For: JITTER DETECTING APPARATUS AND PHASE LOCKED LOOP USING THE  
DETECTED JITTER

**RESPONSE TO RESTRICTION REQUIREMENT**

Commissioner for Patents  
PO Box 1450  
Alexandria, VA 22313-1450

Sir:

This is responsive to the Office Action mailed November 22, 2006, having a shortened period for response set to expire on December 22, 2006, the following remarks are provided.

I. Provisional Election of Claims Pursuant to 37 CFR §1.142

Applicants provisionally elect, with traverse, to prosecute Species I, drawn to claims 1-4, 8, 10 and 11 in response to the preliminary restriction requirement set forth in the Office Action.

II. Applicants Traverse the Requirement

Insofar as Species II is concerned, it is believed that claim 7 is so closely related to elected claims 1-4, 8, 10 and 11 that they should remain in the same application. The elected claims 1-4, 8, 10 and 11 are directed to a jitter detecting circuit, a digital phase locked loop comprising a jitter signal generator, a method of detecting a jitter value and a method of providing a phase locked signal, while claim 7 is directed to a method of determining jitter.

Furthermore, it is noted that the Examiner has already searched and examined all of the